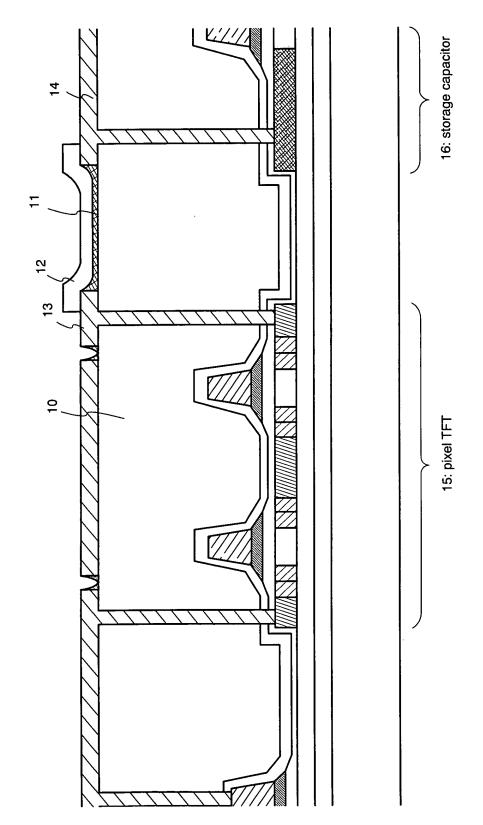
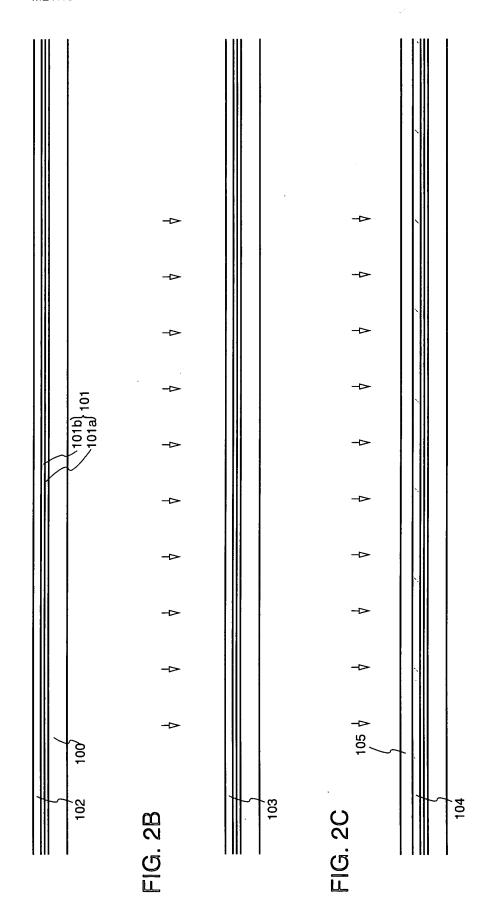
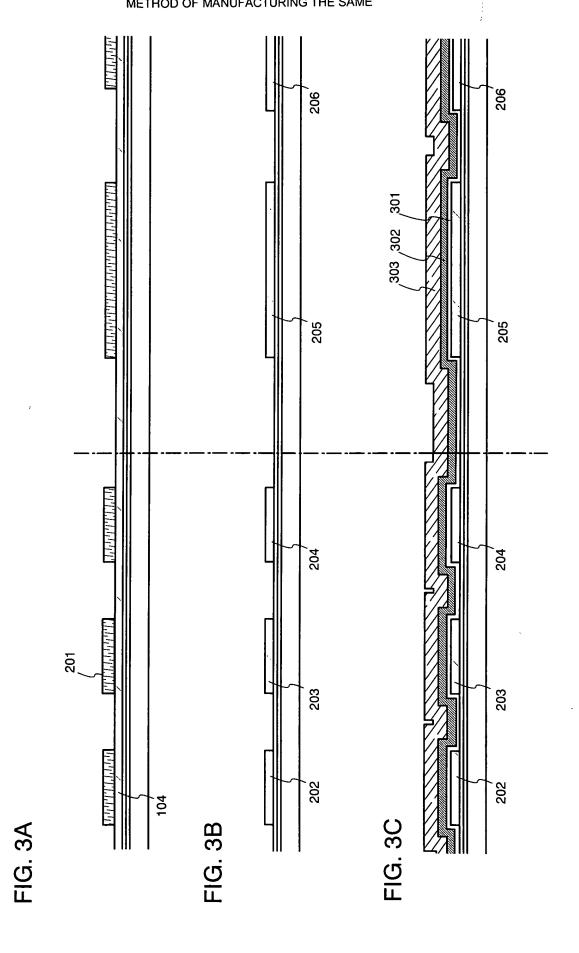
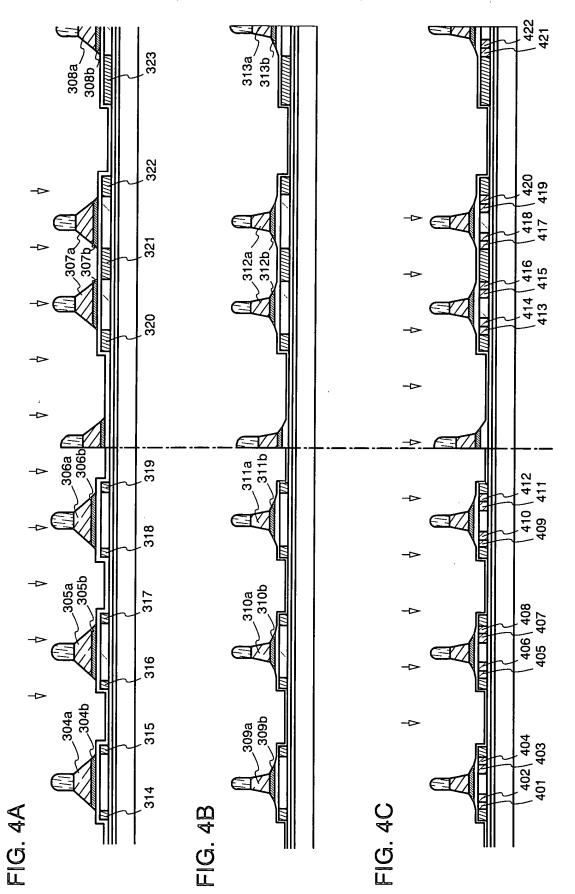
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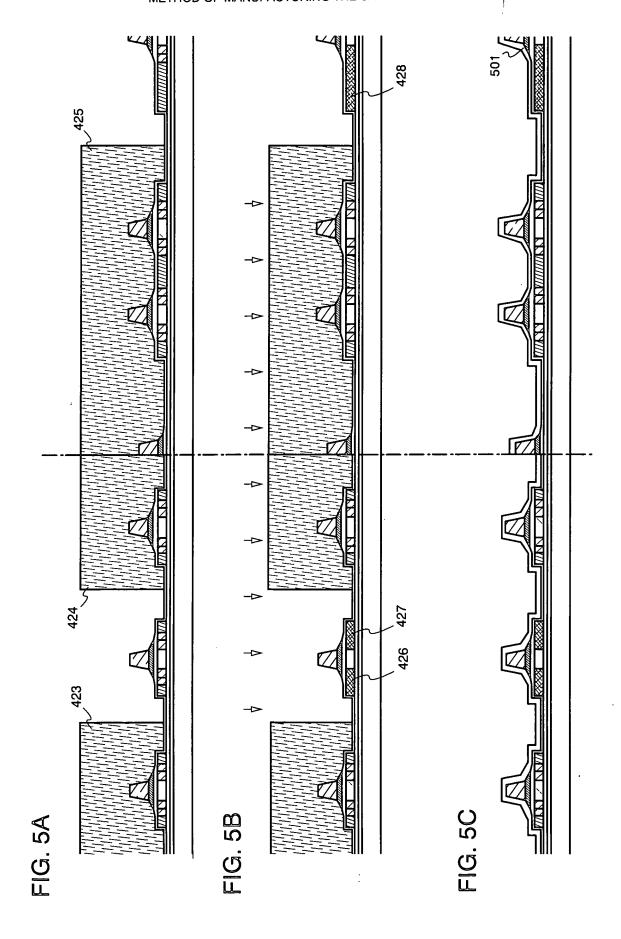


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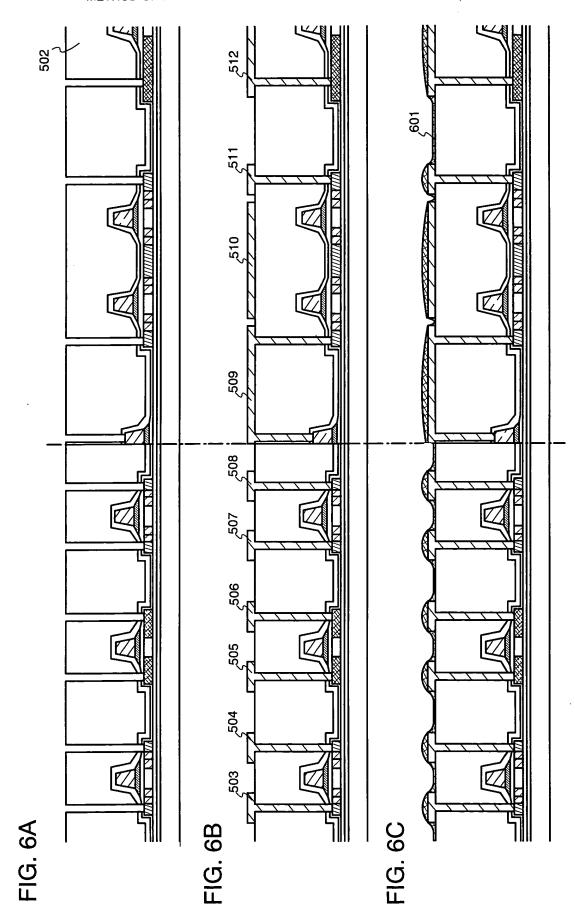




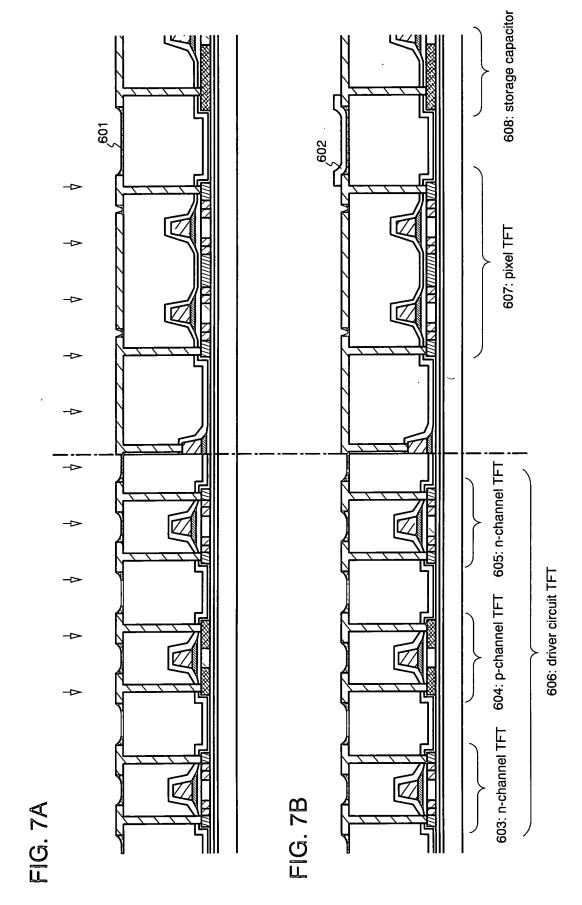




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806: storage capacitor 803 804 805: pixel TFT

FIG. 8 prior art

FIG. 9

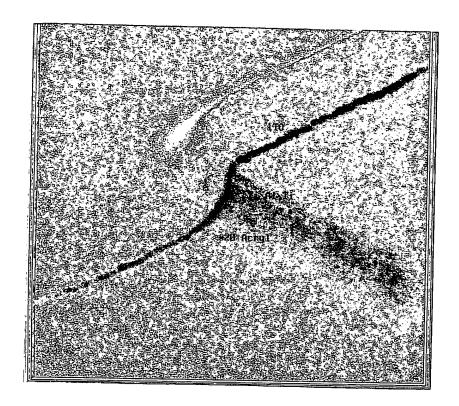
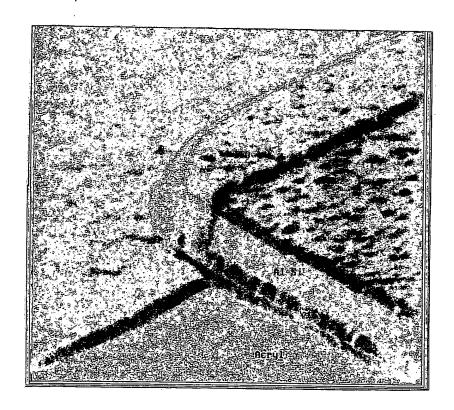
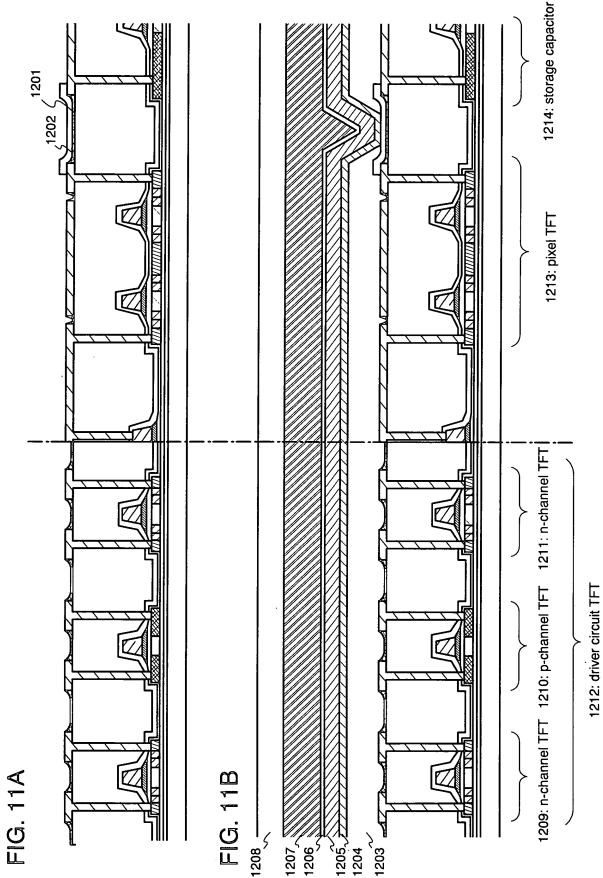


FIG. 10 prior art



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FIG 12

FIC	à. 1	2				_	
					1313: source signal line driver circuit		
					1302: shift register circuit		
					1303: level shifter circuit		
					1304: buffer circuit		
					1305: sampling circuit		
					1310: pre-charge circuit		
1312: gate signal line driver circuit	1306: shift register circuit	1307: level shifter circuit	1308: buffer circuit	1311: first protection circuit	1301: pixel region	1311: first protection circuit	1309: second protection circuit
13							

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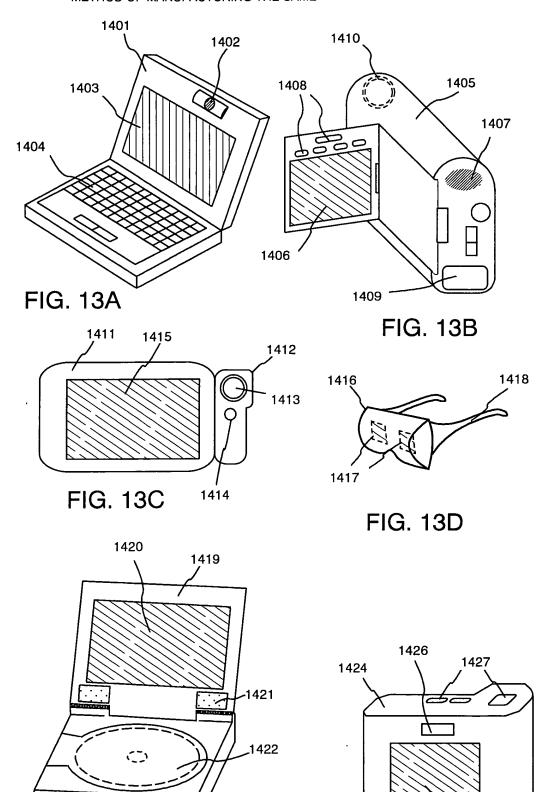


FIG. 13E 1425 FIG. 13F

1423

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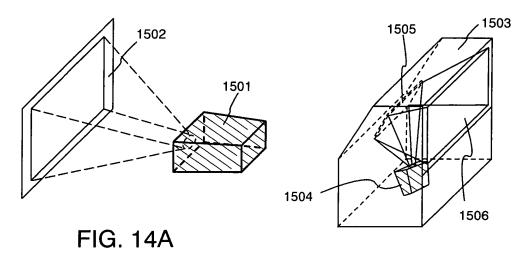


FIG. 14B

